

# **OrCAD Capture Known Problems and Solutions**

**Product Version 17.4-2020  
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This *Known Problems and Solutions* document describes important Cadence Change Requests (CCRs) for OrCAD® Capture and tells you how to solve or work around these problems. For information about CCRs that are fixed for this release, see the *OrCAD Capture: What's New* document.

**Important:** Only the known problems and solutions available at the release time are available in this document.

## **CCR 1147140: Support for reloading of hierarchical part**

**Description:** Any modifications by the librarian in the schematic for hierarchical parts are not updated on choosing the *Reload Library Parts* option. Only the symbol for the hierarchical parts is updated.

**Solution:** Re-launch Capture to view the updated hierarchical parts.

## **CCR 1145501: Library Refresh: Capture does not reload library if added to the project in PM or if lock file exists of same owner**

**Description:** When a library is opened or added to a project in Capture, a `.lck` file is created for that library. If the you replace this library on disk with an updated version, the updates are not read in the same session because the `.lck` file exists for you (owner).

**Solution:** Edit and save the original library in the same session using Capture. Once saved, the library is updated in the Place Part dialog.

## **No CCR: Connection is created between some objects during alignment or distribution**

**Description:** Following objects may connect during alignment or distribution task when the *Drag Connected Object* option is *OFF*:

- Global
- OPC
- Ports
- Power/GND

**Solution:** None

## **CCR 943666 ENH: Flexibility to name a bus member in NetGroup as ?BUS[MSB..LSB]?**

**Description:** Although `Bus [MSB. .LSB]` is allowed resulting in the correct order for input pin of NetGroup block, the pin name and order is incorrect if autowire is done for entry pin or for NetGroup bus. Bus name is not taken into consideration resulting in flat NetGroup name members.

## **CCR 730224: Library gets uprevd without any uprev message.**

**Description:** Open a 16.2 library in 16.3 and perform the following operations:

1. Right-click the part and choose *Split part*. Save the part.

Right-click the library (release 16.2) and choose either *New part from spreadsheet* or *New Symbol*.

For the above operations, the 16.2 library is upgraded without displaying any message.

2. Open a 16.2 library in 16.3 and choose *Save As*. For the upgrade message, choose *Yes* and save it to another name. A backup of the new library is made instead of a backup of the original library.

**Solution:** When upgrading a library (`<library name>.olb`), Capture preserves the original copy of the library in the old database format. This file is saved with the name `<library name>_2_0_0.OBK` at the same location. You can rename this file as `<library name>.OLB` to retrieve the original library.

**CCR 725742: Capture does not generate error and may allow you to create recursive design hierarchy tree.**

**Description:** Capture may allow you to create a recursive design hierarchy tree if a schematic level operation is performed in the Project Manager window.

**Solution:** None.

**CCR 724760: IREF generated for buses connected to OPCs.**

**Description:**

On a design, if the port is a bus and the bus bit is an off-page connector or the net name is the same as the bus bit, IREF is not generated.

If the off-page connector is a bus and the bit is an off-page connector or the net name is the same as the bus bit, IREF is not generated.

**Solution:** None

**CCR 724738: Update/Replace cache not working properly on user-defined pin shapes.**

**Description:** Choose any user-defined pin shape in the design cache and replace it with any other user shape. Changes are reflected in the schematic editor; pin shape changed to the new shape. On editing part, Capture still shows old pin shape and not the new shape.

If you close the part editor and update current or update cache without any change, pin shape is reverted to previous shape. Make some changes and then update, changes are not visible in part editor.

Cleanup cache will even remove the pin shape shown in part editor. But when you try to place same part from design cache still shows the old shape. Move the part or reopen design, changed pin shapes are not retained but reverted to the previous part.

**Solution:** If you do *Replace cache* on a user-defined pin shape say *A* with *B* in the design, all instances of the user-defined pin shape *A* are replaced by *B* in the design and Pin Shape property on the pins are updated to new pin shape value.

This property is an instance override. At any time, you want to revert to library level pin shape value, use *Delete property* in property editor to delete the instance override and the same will be reflected on the schematic.

However, if you do an *Edit part*, it will still show the part level user-defined shape and not the instance override that exists in the schematic. This is by design.

### **CCR 722555: Cannot dock command windows if Allow docking is not set.**

**Description:** The command window is not docked if you set the *Allow docking* option and then de-select the option.

**Solution:** None

### **CCR 701056: Capture crashes due to old workspace data in registry.**

**Description:** Capture crashes choosing *Place – Autowire – Connect to Bus*.

**Solution:** Remove the registry key

HKEY\_CURRENT\_USER\Software\OrCAD\CaptureWorkSpace\16.3.0 and then re-launch Capture.

### **CCR 700463: Pin shape issues.**

**Description:**

- Replace a pin with a user-defined pin shape. The pin is not attached to the part boundary. There is no extra space between the shape and its bounding box.
- The pin shape created and the pin shape shown in part editor are different depending on the shape designed.

**Solution:** None

### **CCR 700407: Pin is removed if using IEEE symbols or picture in pin shapes.**

**Description:** Create a pin shape using IEEE symbols or picture. Replace a pin with this shape, pin is removed. The pin name appears in the corner of the page. Update so that you can check on schematic. On schematic nothing can be seen and the pin is removed.

**Solution:** You cannot use IEEE symbols, text, or images when creating a pin shape.

**CCR 687342: IREF not generated for external designs till page number is manually updated.**

**Description:** Generating IREF for design with single hierarchical block that references an external design (design is in instance mode), causes a page number error. Using the *Annotate* command does not update the pages. Same behavior is observed for a design with hierarchical parts.

**Solution:** Before generating the IREF, update the pages manually.

**CCR 37520: Relative path should be added to place port macros.**

**Description:** The *PORTIN.BAS* (Place Input Port) and *PORTOUT.BAS* (Place Output Port) macros shipped with Capture do not work because they have the wrong path set for *CAPSYM.OLB* library.

**Solution:** Open the *PORTIN.BAS* and *PORTOUT.BAS* files located in the `\tools\capture\macros\` directory in a text editor and correct the path for *CAPSYM.OLB* library.